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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Denise Harmon  
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11000 U.S. PTO  
09/911364  
07/23/01

Applicant : Tony Ivanov, et al.  
Application No. : To be determined  
Filed : To be determined  
Title : METHOD AND STRUCTURE FOR DC AND RF SHIELDING OF INTEGRATED CIRCUITS  
Grp./Div. : To be determined  
Examiner : To be determined  
Docket No. : 42024/MJM/A717

INFORMATION DISCLOSURE STATEMENT  
37 CFR § 1.97(b)

Assistant Commissioner for Patents  
Washington, D.C. 20231

Post Office Box 7068  
Pasadena, CA 91109-7068  
July 20, 2001

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO-1449 listing a reference that is known to applicant. A copy of the listed reference is enclosed. This filing is timely because it is made during one of the periods described in 37 CFR § 1.97(b).

It is respectfully requested that the listed reference be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application. Applicant also requests that an initialed copy of FORM PTO-1449 be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

Respectfully submitted,  
CHRISTIE, PARKER & HALE, LLP

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MJM/sd2

Enclosures: PTO-1449, w/reference

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